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of 1

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Application Number	09/404,923
Filing Date	September 24, 1999
First Named Inventor	Francois Douezy
Group Art Unit	2123
Examiner Name	W. Thomson
Attorney Docket Number	003921.00144

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS

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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
CUR	1.	JP 63-007016 (abstract)	01/12/1988	—	—	—
	2.	JP 4-315209 (abstract)	11/06/1992	—	—	—
	3.	JP 3-248213 (abstract)	11/06/1991	—	—	—
	4.	JP 2-209010 (abstract)	08/20/1990	—	—	—
	5.	JP 1-260518 (abstract)	10/17/1989	—	—	—
	6.	JP 64-46118 (abstract)	02/20/1989	—	—	—
	7.	JP 63-44215 (abstract)	02/25/1988	—	—	—
	8.	JP 60-49421 (abstract)	03/18/1985	—	—	—
	9.	JP 59-9765 (abstract)	01/19/1984	—	—	—
	10.	JP 57-185720 (abstract)	11/16/1982	—	—	—
CUR	11.	JP 56-68813 (abstract)	06/09/1981	—	—	—
CUR	12.	JP 50-72549	06/16/1975	—	—	—
CUR	13.	JP (util. model) 61-133830	08/21/1986	—	—	—
CUR	14.	JP (util. model) 57-148231	—	—	—	—

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CUR	15.	Mel Bazes, "An Interpolating Clock Synthesizer," IEEE J. of Solid State Circuits, Vol. 31, No. 9, pp. 1295-1301 (September 1996).	—
CUR	16.	International Search Report for PCT/US00/03261, issued June 23, 2000.	—
CUR	17.	Translation of October 28, 2003 Office Action in Japanese application 2001-500405.	—

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